



07/18/00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of: Joseph E. Geusic et al.

Title: INTEGRATED CIRCUITS USING OPTICAL WAVEGUIDE INTERCONNECTS FORMED
THROUGH A SEMICONDUCTOR WAFER AND METHODS FOR FORMING SAME

Attorney Docket No.: 303.382US2

PATENT APPLICATION TRANSMITTAL

BOX PATENT APPLICATION

Commissioner for Patents
Washington, D.C. 20231

We are transmitting herewith the following attached items and information (as indicated with an "X"):

- ☒ Return postcard.
- ☒ **DIVISIONAL** of prior Patent Application No. 09/031,961 (under 37 CFR § 1.53(b)) comprising:
- ☒ Specification (21 pgs, including claims numbered 1 through 44 and a 1 page Abstract).
 - ☒ Formal Drawing(s) (4 sheets).
 - ☒ Signed Declaration (5 pgs).
 - ☒ Signed Power of Attorney (1 pg.)
 - ☒ Incorporation by Reference: *The entire disclosure of the prior application, from which a copy of the oath or declaration is supplied herewith, is considered as being part of the disclosure of the accompanying application and is hereby incorporated by reference therein.*
 - ☒ Check in the amount of \$690.00 to pay the filing fee.
- ☒ Prior application is assigned of record to Micron Technology, Inc.
- ☒ Information Disclosure Statement (1 pgs), Form 1449 (4 pgs). References NOT enclosed, cited in prior application.
- ☒ Preliminary Amendment (2 pgs).

The filing fee has been calculated below as follows:

	No. Filed	No. Extra	Rate	Fee
TOTAL CLAIMS	17 - 20 =	0	x 18 =	\$0.00
INDEPENDENT CLAIMS	2 - 3 =	0	x 78 =	\$0.00
[] MULTIPLE DEPENDENT CLAIMS PRESENTED				\$0.00
BASIC FEE				\$690.00
TOTAL				\$690.00

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I hereby certify that this paper or fee is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and is addressed to the Commissioner for Patents, Box Patent Application, Washington, D.C. 20231.

By: Shawn L. HiseSignature: [Signature]